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29 within said RTS periods ; and
30 means for multiplying [by a factor of N] the frequency of the pulse
31 signal generated by said converting means by the same factor of an integer N used
32 in said first dividing means for recovering the timing clock of the service input.

Remarks

The present application stands with claims 1-10 rejected under 35 U.S.C. 112, second paragraph. The disclosure was also objected to in view of the informalities noted by the Examiner in paragraph 2 of his Office Action.

The specification has been amended to correct the informalities noted by the Examiner. The acronym "SRTS", which stands for synchronous residual time stamp (part of the title of the invention, but not defined and not used as an acronym in the specification), was incorrectly used in the specification rather than "SFET". From the preceding sentences, it can be seen that "SFET" is what was intended to be used.

All the Examiner's comments under 35 U.S.C. 112, second paragraph, regarding the language of claims 1, 3, 5 and 8, have been incorporated into these claims as amended hereinabove. The Examiner has noted that claims 1-10 would be allowable if amended to overcome the rejection under 35 U.S.C. 112. It is believed that the objected to claims are now allowable and that passage to issue of the subject application is respectfully requested.

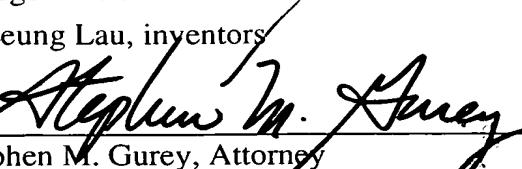
If for any reason the Examiner feels that the present application is not in a condition for allowance and that a telephone interview would be helpful, he is invited to call applicants' undersigned attorney at (201)740-6430.

Respectfully submitted,

Paul Egon Fleischer

Chi-Leung Lau, inventors

By


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